

# INTERNATIONAL SEARCH REPORT

Int. Application No.

PCT/NL 03/00933

**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 G06F13/40

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, IBM-TDB

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 598 106 B1 (OTTO ERICH S ET AL) 22 July 2003 (2003-07-22) abstract column 1, line 30 - column 2, line 10 column 3, line 36 - line 62 column 5, line 34 - column 9, line 48 column 10, line 28 - column 12, line 54 figures 2,4-6,8,10	1-22
X	US 2002/059485 A1 (GODICKE THOMAS ET AL) 16 May 2002 (2002-05-16) the whole document	1-22
X	US 5 870 573 A (JOHNSON LEITH L) 9 February 1999 (1999-02-09) the whole document	1-22

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☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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- \*E\* earlier document but published on or after the international filing date
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Date of the actual completion of the international search

5 August 2004

Date of mailing of the international search report

23/08/2004

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In: International Application No

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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A	LAJOLO M ET AL: "Early evaluation of bus interconnects dependability for system-on-chip designs" IEEE CONFERENCE PROCEEDINGS ARTICLE, 3 January 2001 (2001-01-03), pages 371-376, XP010531481 page 371 - page 373 -----	1-22
A	VRANESIC Z G ET AL: "Hector-a hierarchically structured shared memory multiprocessor" IEEE CONFERENCE PROCEEDINGS ARTICLE, vol. i, 8 January 1991 (1991-01-08), pages 444-453, XP010093276 the whole document -----	1-22
A	US 2001/004748 A1 (NYGREN AARON) 21 June 2001 (2001-06-21) the whole document -----	1-22
A	US 6 421 348 B1 (PAGNON VICKIE ET AL) 16 July 2002 (2002-07-16) abstract figure 1 -----	1-22
A	US 5 996 040 A (MALGIOGLIO FRANK ET AL) 30 November 1999 (1999-11-30) abstract figure 1 -----	1-22
A	US 3 984 819 A (ANDERSON GEORGE A) 5 October 1976 (1976-10-05) abstract figure 8 -----	1-22

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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